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List of Patents and Publications for Applicant's
 INFORMATION DISCLOSURE STATEMENT
 (Use several sheets if necessary)

Applicant
Strongin et al.

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OFFICE OF PETITIONS

U.S. Patent Documents

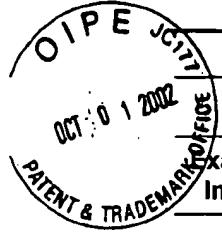
See Page 1

Foreign Patent Documents

See Page 1

Other Art

See Page 1



U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1			RECEIVED			
	A2			MAR 10 2003			RECEIVED
	A3			Technology Center 2100			OCT 03 2002
	A4			Technology Center 2100			Technology Center 2100
	A5						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						
	B3						
	B4						
	B5						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
BL	C1	Intel, "Low Pin Count (LPC) Interface Specification Revision 1.0," pp. 1-31 (09/29/97)
BL	C2	Standard Microsystems Corporation, "100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Application," Part No. LPC47B37x, pp. 1-254 (06/17/99)
BL	C3	FIPS Pub 140-1 Federal Information Processing Standards Publication, "Security Requirements for Cryptographic Modules" (01/11/1994)
BL	C4	Intel, "Communication and Networking Riser Specification," Revision 1.0 (02/07/2000)
BL	C5	"Handbook of Applied Cryptography" CRC Press 1997 pp. 154 - 157, 160 - 161, 191 - 198, 203 - 212

EXAMINER:

DATE CONSIDERED: 7/1/04

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